

3A Capable Slew Rate Controlled Load Switch With True Reverse Current Blocking.

Features

- Input Voltage Range 1.5V to 5.5V
- Typical R_{on}:

20m Ω at V_{IN} =5.5V

21m Ω at V_{IN} =4.5V

 $38m\Omega$ at $V_{IN}=1.8V$

48mΩ at $V_{IN}=1.5V$

- Slew Rate/inrush Control with t_R = 3.3ms(typical)
- 3A Continuous Operating Current
- Low Quiescent Current Less than 1µA
- True Reverse Current Blocking(TRCB)
- Logic CMOS IO meets JESD78 standard for GPIO interface and related power supply requirement.
- The package is WLCSP2X3 -6

Applications

- Cellular Phones
- Portable Navigation Devices(PND)
- Personal Media Players (PMP)
- Ultra Mobile PCs
- Portable Instrumentation
- Other Portable Applications
- PDAs
- Industrial and DataComm Equirment

General Description

The G5197 advanced load-management switches target applications requiring a highly integrated solution. If disconnects loads powered from DC power rail (<6V) with stringent off-state current targets and high load capacitances (up to 200µF). Each switch consists of slew-rate controlled low-impedance MOSFET switch (23m Ω type) and other integrated analog features. The slew-rate controlled turn-on characteristic prevents inrush current and the resulting excessive voltage droop on power rails.

The G5197 has TRCB function blocking unwanted reverse current from V_{OUT} to VI_N during ON/OFF state.

These device have exceptionally low off state current drain (<2 μ A max) which facilitate compliance in very low stand by power applications. The input voltage range operates from 1.5V to 5.5V DC to support a wide range of applications in consumer. Optical, medical storage, portable and industrial device power management. Switch control is managed by a logic input (Active HIGH) capable of interfacing directly with low voltage control signal/GPIO with no extremely pull-down resistor required.

The device is packaged in WLCSP2X3 -6 pin.

Ordering Information

ORDER NUMBER	MARKING	SPEC	PACKAGE (Green)
G5197B21U	517 xx	No output discharge, Ron is $21m\Omega$ at $4.5~V_{IN}$, T_R =3.3ms, On pin activity is high.	WLCSP2X3-6

Note: B2: WLCSP2x3-6 1: Bonding Code U: Tape & Reel

Pin Configuration

